

## About the Institution

### Vision

Progress through Education

### Mission

To seek, strive for and scale greater heights of quality education

**Vidya Academy of Science and Technology (VAST)** established in 2003, is a self-financing engineering college affiliated to APJ Abdul Kalam Technological University, Kerala with competent and committed faculty, top notch infrastructure and high technology teaching aids. The College offers B.Tech programmes in six streams of engineering, M.Tech programmes in two specializations, MCA programme and Ph.D programmes in Engineering and Mathematics. VAST has a holistic approach to education where academic training goes hand in hand with social commitment.



## About the Department

### Our Vision

“To be a centre of excellence in electronics and communication engineering through value-based education and research”

### Our Mission

**M1:** To provide students state-of-the-art academic ambiance for quality education.

**M2:** Facilitate interdisciplinary learning, industry interactions and research initiatives for long term career development goals.

**M3:** To educate and mentor students on professional responsibilities, ethical values and lifelong learning.

The Department of **Electronics and Communication Engineering (ECE)** currently offers B. Tech in Electronics and Communication Engineering as well as M.Tech in Embedded Systems. The department motivates both technical and extracurricular activities for the overall development of its students and staff. The department invariably strives for excellence and innovation in education, research, professional service and social outreach to evolve into a nationally recognised organisation.

### Convener

**Dr. S. Swapna Kumar**, Professor and HoD,  
ECE Dept.

### Coordinators

**Mr. Santumon S D**, Asst. Professor, ECE Dept.  
Mob no: +91 9544150505.

**Ms. Snehalatha O J**, Asst. Professor, ECE Dept.  
Mob no: +91 8113906190

## Three -Day Faculty Development Programme [Hybrid Mode] on

## “Practical Aspects of Communication Engineering with Hands-on”

Organised by  
**Department of Electronics and  
Communication Engineering**

**22nd - 24th February 2024**



in association with  
**IQAC, ISTE and IE(I)**



**Vidya Academy of Science and Technology**  
Thalakkottukara P.O, Thrissur, Kerala - 680501  
[www.vidyaacademy.ac.in](http://www.vidyaacademy.ac.in)

## Objective of the FDP

The Faculty Development Program (FDP) on **"Practical Aspects of Communication Engineering with Hands-on"** is intended to provide educators, researchers, and professionals with practical insights, hands-on experience, and updated knowledge in some relevant areas of communication engineering. The FDP also aims to empower educators with the practical skills and knowledge needed to prepare their students for successful careers in communication engineering, aligning academic programs with industry demands and technological advancements.

## Resource Persons



**Er. Gouthaman P K**

Senior Director & Head of Network Resource Management (Retd.), Etisalat, UAE



**Dr. S. Swapna Kumar**

Professor & Head, Department of ECE, VAST, Thrissur



**Dr. Rajesh G**

Assistant Professor, Department of ECE, IIIT, Kottayam



**Er. T K Kuttappan**

Consultant (Telecom NW & OPGW) Former Engineer (Transmission), Etisalat, UAE

## Programme Schedule

### Day 1 [Online Mode]

- Inaugural Session [9.30am-10am]
- Telecom Operation Management [10am-12.30pm]
- Energy-Efficient WSN and IoT Communication for Conceptual Practical Application [1.30pm-3.30pm]

### Day 2 [Offline Mode]

- Python For Communication Engineering- Session 1 [9.30am-12.30pm]
- Python For Communication Engineering - Session 2 (Hands-on) [1.30pm-4.00pm]

### Day 3 [Offline Mode]

- OFC in Telecommunication Network - Session 1 [9.30am-12.30pm]
- OFC in Telecommunication Network - Session 2 (Hands-on) [1.30pm-3.30pm]
- Valedictory session [3.30 pm- 4pm]

## Who should Apply?

Faculty members, Research Scholars, M.Tech and final year B.Tech students of AICTE approved institutions focusing on the Communication engineering sector can apply for the FDP.

## Selection Process

Participants will be admitted on 'First - come - first - serve' basis. Participation is limited to fifty seats. Selection to the FDP will be intimated through email only.

## Registration Fee

A fee of **Rs 200/-** for external participants. Payment can be done via Google Pay to the mobile number:

**+91 9544150505.**

After completion of payment, transaction details must be uploaded in the registration link.

## How to Apply?

Participants can register for the FDP by clicking on the QR code or the link given below.

<https://forms.gle/JqE7VFCp5PwpypbbAt>



Click on QR code to Register

**Last Date to Apply: 18th February 2024**

**Dates of Sessions: 22nd, 23rd and 24th February 2024**





## Department of Electronics and Communication Engineering

Vidya Academy of Science and Technology, Thrissur

in association with IQAC, ISTE and IE(I)

### Organises

Three-Day FDP on  
[Hybrid Mode]

### “Practical Aspects of Communication Engineering with Hands-on”

22<sup>nd</sup> -24<sup>th</sup> February 2024

### FDP Schedule

Date	Time	Mode of Session	Resource Person	Topic
22/2/2024 Thursday	9.30 am- 10 am	Inaugural Session		
	10 am-12.30 pm (FN)	Online	Er. Gouthaman P K	Telecom Operation Management
	1.30 pm- 3.30 pm (AN)	Online	Dr. S. Swapna Kumar	Energy-Efficient WSN andIoT Communication for Conceptual Practical Application
23/2/2024 Friday	9.30 am- 12.30 pm (FN)	Offline	Dr. Rajesh G	Python For Communication Engineering- Session 1
	1.30 pm- 4.00 pm (AN)	Offline		Python For Communication Engineering – Session 2 (Hands-on)
24/2/2024 Saturday	9.30 am- 12.30 pm (FN)	Offline	Er. T K Kuttappan	OFC in Telecommunication Network - Session 1
	1.30 pm- 3.30 pm (AN)	Offline		OFC in Telecommunication Network - Session 2 (Hands-on)
	3.30 pm- 4 pm	Valedictory session and certificates distribution		

### FDP Coordinators