

Department Vision

“To be a centre of excellence in electronics and communication engineering through value-based education and research”

Department Mission

M1: To provide students state-of-the-art academic ambiance for quality education.

M2: Facilitate interdisciplinary learning, industry interactions and research initiatives for long term career development goals.

M3: To educate and mentor students on professional responsibilities, ethical values and lifelong learning.

About the Institution

Vision

Progress through Education

Mission

To seek, strive for and scale greater heights of quality education

Vidya Academy of Science and Technology (VAST) established in 2003, is a self-financing engineering college affiliated to APJ Abdul Kalam Technological University, Kerala with competent and committed faculty, top notch infrastructure and high technology teaching aids. The College offers B.Tech programmes in six streams of engineering, M.Tech programmes in two specializations, MCA programme and Ph.D programmes in Engineering and Mathematics. VAST has a holistic approach to education where academic training goes hand in hand with social commitment.



Objective of the FDP

This Faculty Development Program (FDP) is intended to provide educators, researchers, and professionals with industrial insights to update knowledge in relevant areas of IoT, Robotics and Communication Engineering. The FDP also aims to empower educators with the skills and knowledge needed to prepare their students for successful careers in communication engineering, aligning academic programs with industry demands and technological advancements.

Advisory Panel

Dr. Shalij P R, Academic Director

Dr. Sunitha C, Principal

Dr. S Swapna Kumar, Head of ECE Dept.

FDP Coordinators

Dr. Abitha M A., Associate Professor, ECE Dept.

Mob: +91 9605784912

Mr. Anil M, Asst. Professor(Sel. Grade),
ECE Dept. Mob: +91 8086850021

About the Department

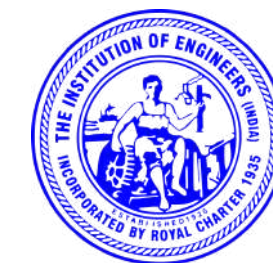
The Department of Electronics and Communication Engineering (ECE) currently offers B. Tech in Electronics and Communication Engineering as well as M.Tech in Embedded Systems. The department motivates both technical and extracurricular activities for the overall development of its students and staff. The department invariably strives for excellence and innovation in education, research, professional service and social outreach to evolve into a nationally recognised organisation.

Six -Day International Faculty Development Programme [Hybrid Mode] on

‘ NGCF-IS ’ Next-Generation Communication Framework for Intelligent Systems

Organised by
**Department of
Electronics and Communication
Engineering**
from

24th to 29th November 2025



IQAC, IE(I) and ISTE VAST Chapters



Vidya Academy of Science and Technology

Thalakkottukara P.O, Thrissur, Kerala - 680501

www.vidyaacademy.ac.in

Resource Persons

- Dr. Harigovindan V P, Professor & Dean(Planning & Development.), NIT, Puducherry
- Mr. Zaher Abdulrahman, Electronics Lead, Jaguar Land Rover Limited, United Kingdom
- Dr. Serin V Simpson, Asst. Professor, Presidency University, Bengaluru
- Mr. Anil M, Asst. Professor (Sel. Grade), VAST, Thrissur
- Dr. C K Raju, Head of Edu Programs, IIIT, Hyderabad
- Dr. S Swapna Kumar, Professor & HOD, VAST, Thrissur
- Dr. A Sathesh, Post Doctoral Fellow, Amrita Vishwa Vidyapeetam, Coimbatore
- Mr. Prasanth K Prasad, Director, ExoDyne Pvt Ltd., Thiruvananthapuram
- Dr. Abitha M.A, Asso. Professor, VAST, Thrissur
- Ms. Jemy Jose Kakkassery, Asst. Professor, VAST, Thrissur
- Ms. Honey Mol P K, Asst. Professor (Sr. Grade), VAST, Thrissur
- Dr. Dhanya V S, Scientist-E, C-DAC, Thiruvananthapuram

Programme Schedule

Day 1 (24/11/2025)

Inaugural Session [12:50pm-1:00pm]

Session 1 [1:00pm-2:30pm]

NOMA IoT Networks for 5G and Beyond -
Dr. Harigovindan V P

Session 2 [2:35pm-3:45pm]

Embedded Electronics in brake-by-wire: A shift
towards smarter braking systems -
Mr. Zaher Abdulrahman

Day 2 (25/11/2025)

Session 3 [1:00pm-2:30pm]

Cyber Resilience: Strengthening Security Through
Testing - Dr. Serin V Simpson

Session 4 [2:30pm-3:45pm]

Electrospinning of Nanostructures for Advanced
Material Applications - Mr. Anil M

Day 3 (26/11/2025)

Session 5 [1:00pm-2:30pm]

Using Open Pre-trained AI Models for Research -
Dr. C K Raju

Session 6 [2:30pm-3:45pm]

Secure and Efficient IoT integrating WSN,
Blockchain and Federated Learning -
Dr. S Swapna Kumar

Day 4 (27/11/2025)

Session 7 [1:00pm-2:30pm]

Deep Learning based Architecture for Image
reconstruction in Industrial Tomography
Applications - Dr. A Sathesh

Session 8 [2:30pm-3:45pm]

From Idea to Impact: Navigation Solutions to the
Indian Startup Surge - Mr. Prasanth K Prasad

Day 5 (28/11/2025)

Session 9 [1:00pm-2:30pm]

Intelligent Motion Planning for robot systems in
Dynamic Environments - Dr. Abitha M A

Session 10 [2:30pm-3:45pm]

Exploring Python based Solutions for Intelligent
Communication Networks -
Ms. Jemy Jose Kakkassery

Day 6 (29/11/2025)

Session 11 [1:00pm-2:30pm]

Artificial Intelligence for Next Generation EVs -
Ms. Honey Mol P K

Session 12 [2:30pm-3:45pm]

Edge AI: Running Machine Learning Models in
IOT Devices - Dr. Dhanya V S

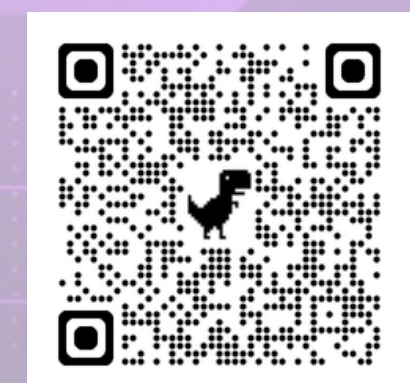
Feedback & Valedictory Function
[3:45pm-3:55pm]

Who should Apply?

Faculty members, Research Scholars and M.Tech students of AICTE approved institutions focusing on the Communication engineering sector can apply for the FDP.

How to Apply?

Participants can register for the FDP by scanning the given QR code or by clicking in the link given below. **There is a fee of \$5 for foreign participants, Rs 200 for Indian participants and free for internal participants. Payment can be done via Google Pay to the mobile number: +918086850021.** After completion of payment, transaction details must be uploaded in the registration link.



<https://forms.gle/Rh6J3WhRRgHmXoi97>