

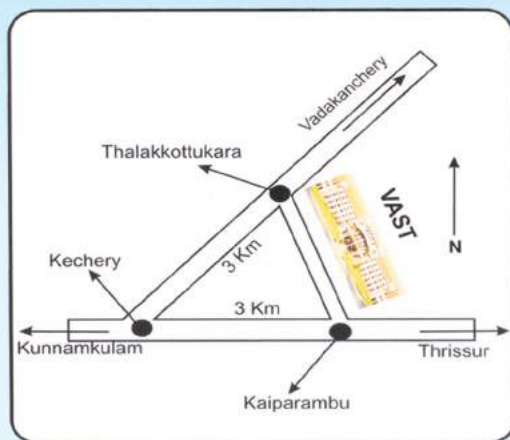


**VIDYA ACADEMY OF SCIENCE & TECHNOLOGY**  
**THALAKKOTTUKARA**  
**THRISSUR, KERALA – 680501**

Vidya Academy of Science and Technology (VAST), is a new generation self-financing engineering college, administered by Vidya International Charitable Trust (VICT). The college is affiliated to University of Calicut, approved by AICTE and certified by ISO 9001:2008 certification.

The department of ECE was established in 2003. It offers a 4 years B.Tech in ECE and 2 years M.Tech in Embedded Systems programme. It has distinguished team of faculty and the department is equipped with high quality technology infrastructure.

### Route Map



**CONVENER**  
**Dr. S. Swapna Kumar**  
Professor & Head of Department  
Department of ECE,VAST  
09995493058

**CO-ORDINATORS**  
**Mrs. HoneyMol P K**  
Department of ECE,VAST  
Assistant Professor  
09946372380

**Mrs. Divya Unni**  
Department of ECE,VAST  
Assistant Professor  
09446153508

**E-MAIL:** [eeassociation@vidyaacademy.ac.in](mailto:eeassociation@vidyaacademy.ac.in)

### REGISTRATION

**Registration Fee :** ₹500/- (External Participants)  
₹300/- (Internal Participants)

**Mode of Payment :** Online (NEFT/IMPS)  
**A/C Name :** ECE Technical Cell  
**A/C No :** 240301000002449  
**Name of Bank :** Indian Overseas Bank  
**Branch :** Thalakkottukara  
**IFSC Code :** IOBA0002403

**Registration Link:** [www.vidyaacademy.ac.in](http://www.vidyaacademy.ac.in)

### IMPORTANT DATES

**Last Date of Registration :** 27<sup>th</sup> February 2017  
**Intimation of Selection :** 28<sup>th</sup> February 2017  
**Date of Programme :** 2<sup>nd</sup> - 4<sup>th</sup> March 2017

### Resource Person

**Mr. Pankaj Kumar G**  
**Asst. Professor**  
**FISAT, Angamaly, Ernakulam**



**Faculty Development Programme**

**on**

**“PYTHON PROGRAMMING IN  
IMAGE PROCESSING & MACHINE  
LEARNING”**

**2<sup>nd</sup> to 4<sup>th</sup>**

**March**

**2017**



**Organised by:**

**DEPARTMENT OF**

**ELECTRONICS & COMMUNICATION ENGINEERING**

*In association with*



**Vidya Academy of Science & Technology**

(A unit of Vidya International Charitable Trust)

Approved by AICTE and Affiliated to APKATU & University of Calicut

An ISO 9001-2008 Certified Institution

Thalakkottukara P.O., Thrissur, Kerala – 680 501

Tel: +91-4885 287751 / 287752, Fax : +91-4885 288366

website : [www.vidyaacademy.ac.in](http://www.vidyaacademy.ac.in)



### About ECED

The Department of Electronics and Communication Engineering (ECE) provides an unrivaled education in Electronics and Communication engineering and Embedded System (ES) to our graduates and post graduate students. Department started Under Graduation programme with an intake of 60 in the year 2003. The Post Graduation Programme was started with an intake of 18 in the year 2013.

The Department of ECE (PG) - Embedded System Design is recognized as the research center with all computing facilities, State-of-the-art laboratories, and efficient teaching professionals and well stacked department library. Presently, Department of Electronics & Communication Engineering offers a 4 years B.Tech in ECE with an intake of 120 regular students, 12 lateral entry students in the 3rd semester and 2 years M.Tech in Embedded Systems programme with an intake of 24 students.

### About FDP

Python is a programming language that lets you to work more quickly, integrate your systems effectively, gain more productivity and with lower maintenance costs. Python has emerged as an excellent choice for scientific computing because of its simple syntax, ease of use, and elegant multi-dimensional array arithmetic. Hands on training mode in this workshop will help the participants to master the use of Python for Engineering Problems.

The Topics covered are:

What is Python

Setting up Python

Understand the basic structure of a Python program

Image processing using Open CV

Machine Learning

### Session Details

#### DAY 1 (02-03-2017)

08:30 – 09:00 AM	: Registration
09:00 – 09:30 AM	: Inauguration
09:30 – 12:30 PM	: Basics of Python - Part I
12:30 – 01:30 PM	: Lunch Break
01:30 – 04:20 PM	: Basics of Python - Part II

#### DAY 2 (03-03-2017)

09:00 – 12:30 PM	: Basics of OpenCV
12:30 – 01:30 PM	: Lunch Break
01:30 – 04:20 PM	: Object detection & Image stitching

#### DAY 3 (04-03-2017)

09:00 – 12:30 PM	: Face recognition
12:30 – 01:30 PM	: Lunch Break
01:30 – 04:20 PM	: Machine Learning with Python

### Who can Apply?

Faculty from AICTE approved Engineering colleges, industries and R&D organisations can participate. PG students in related discipline are also eligible.

### Accommodation

Participants have to make their own arrangements for travel, boarding, lodging, etc. However, participants can make use of college buses in the normal service routes and accommodation in hostel can be arranged on request in advance, for a limited number.

